



*Short Status Report*

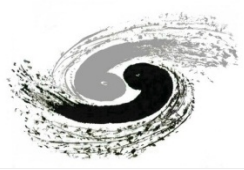
# CGEM project

Collaboration Meeting (Beijing)

June 4-7th, 2013

# Outline

- ❖ CGEM simulation at BESIII
- ❖ Space available for CGEM installation
- ❖ CGEM prototype project
- ❖ Roadmap toward a CGEM-based IT
- ❖ Collaboration activities from IHEP side (proposal)
- ❖ Next step for INFN – IHEP both sides (proposal)

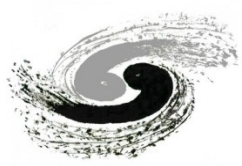


中国科学院高能物理研究所  
INSTITUTE OF HIGH ENERGY PHYSICS

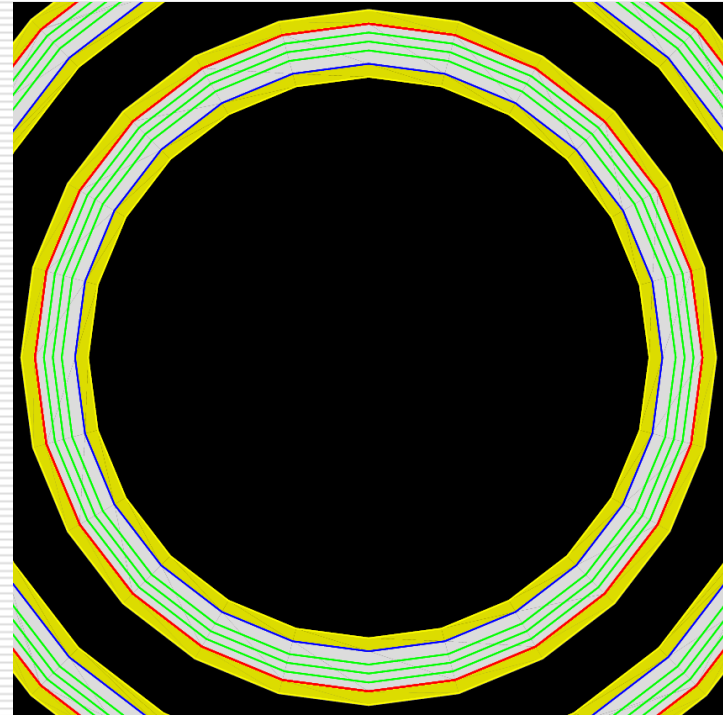
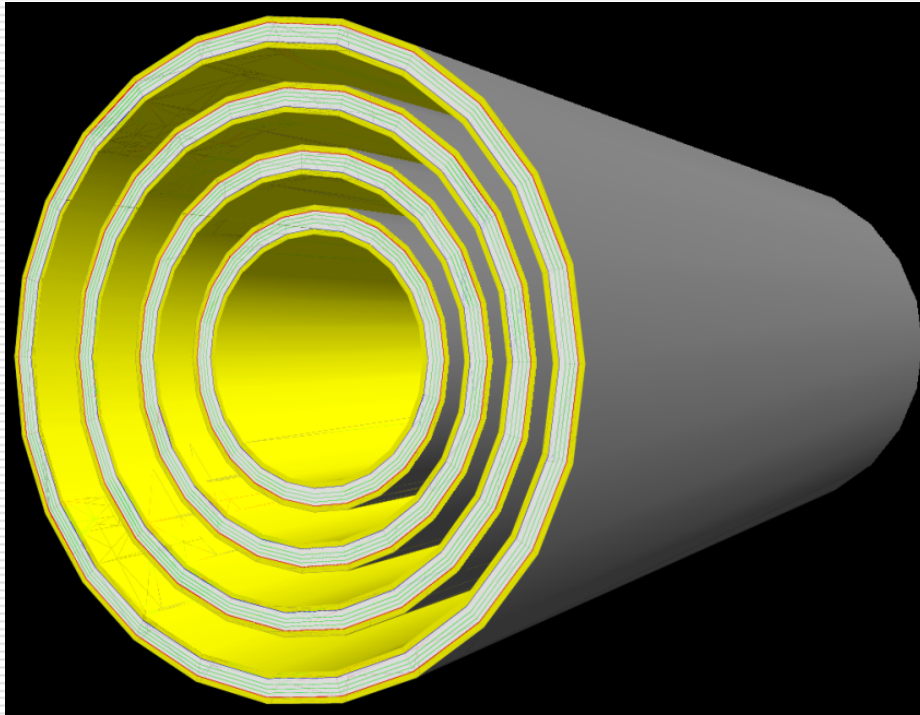
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# CGEM simulation at BESIII

*Dong Mingyi, Wang Liangliang, Xiu Qinglei*



# CGEM Geant4 Model



**Yellow: Honeycomb Support (Nomex)**

**Blue: Cathode (Cu, Kapton)**

**White: Work Gas of Gem (Ar:CO<sub>2</sub>=70:30)**

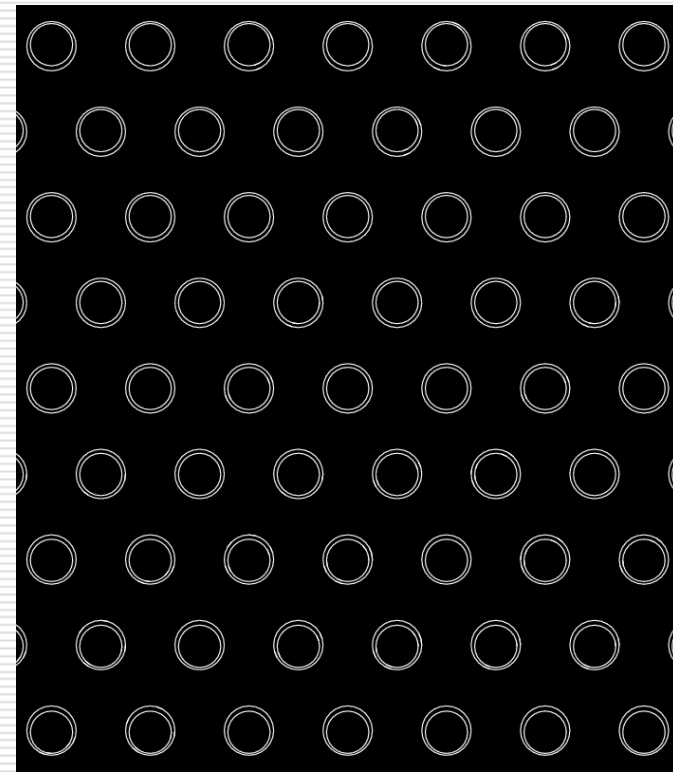
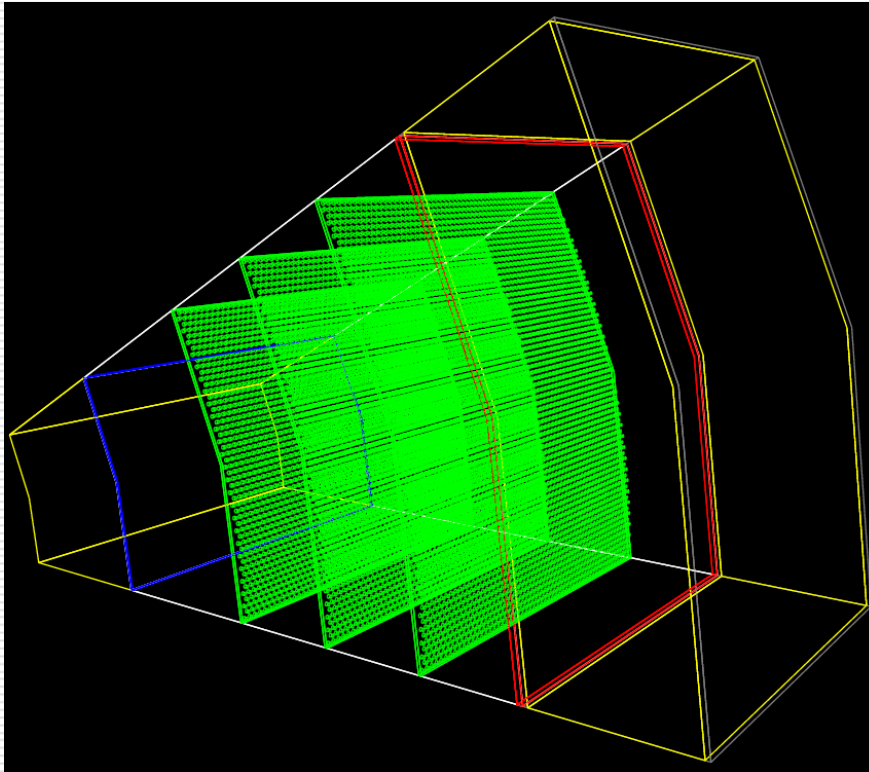
**Green: Gem Foil (Cu, Kapton)**

**Red: Readout Anode (Cu, Kapton, Al Shielding)**

**Grey: Carbon Fiber Support**



## Detail Structure of CGEM Layer



Single Mask Gem Foil, Single conical hole structure

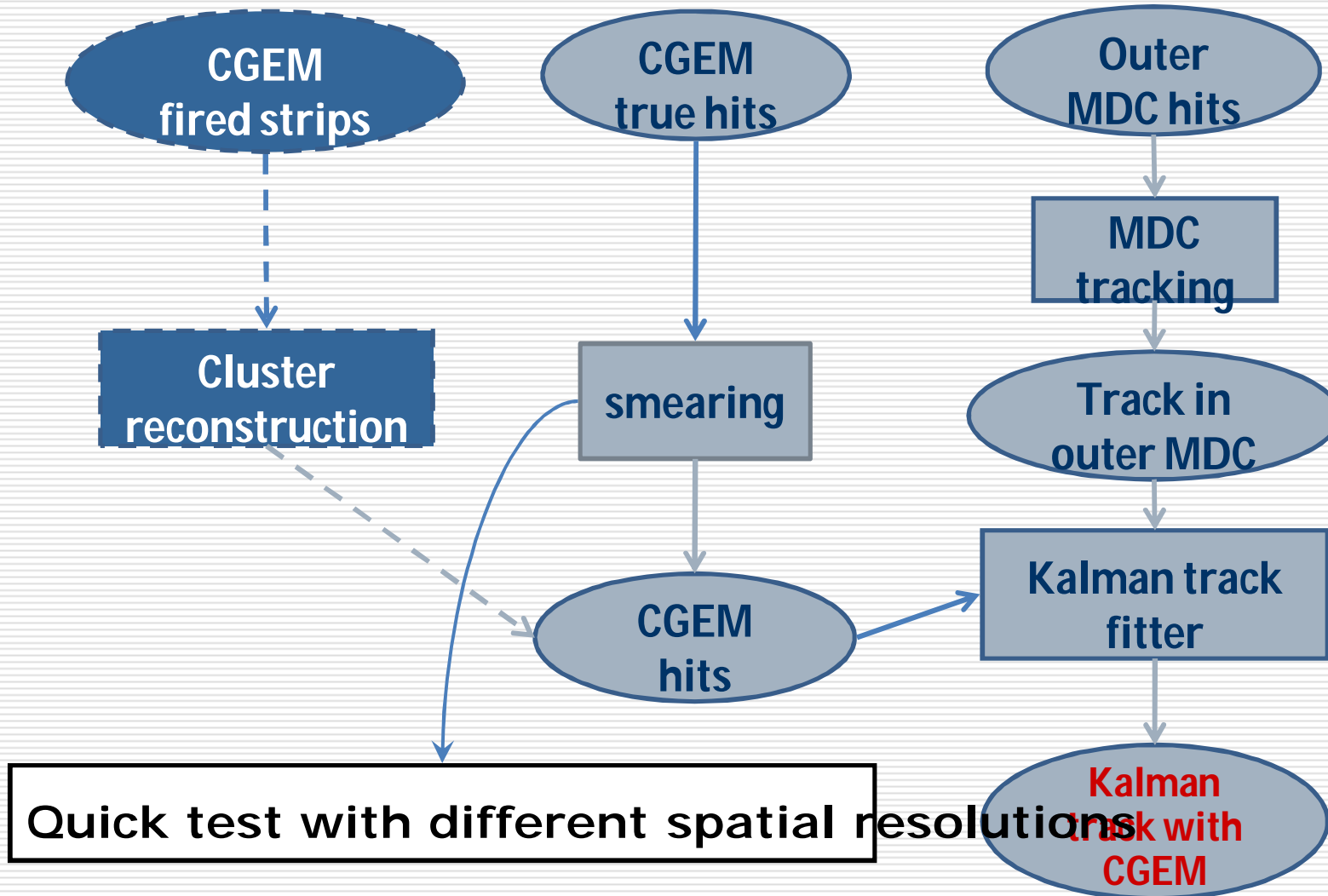
Hole diameter: 70-60  $\mu\text{m}$

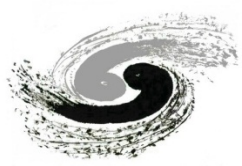
The larger section of the holes facing the cathode.

Hole Pitch: 140 $\mu\text{m}$

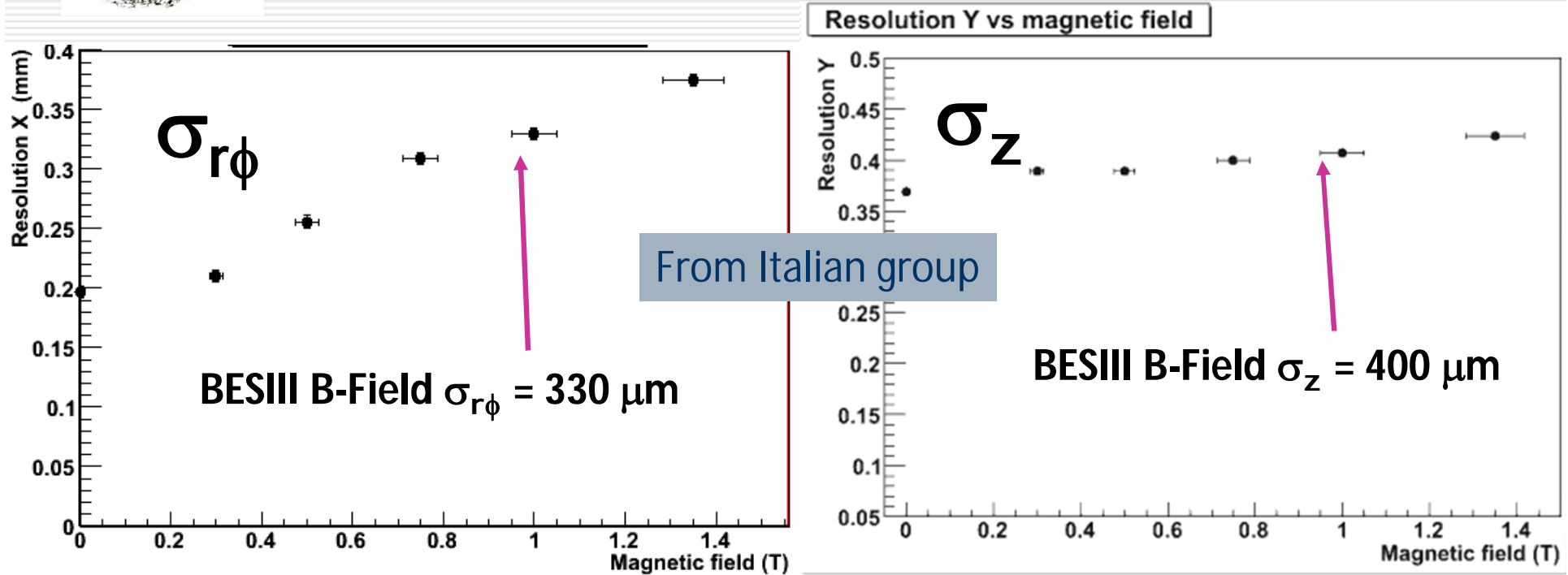


# Preliminary reconstruction with CGEM inner detector





# Expected spatial resolution

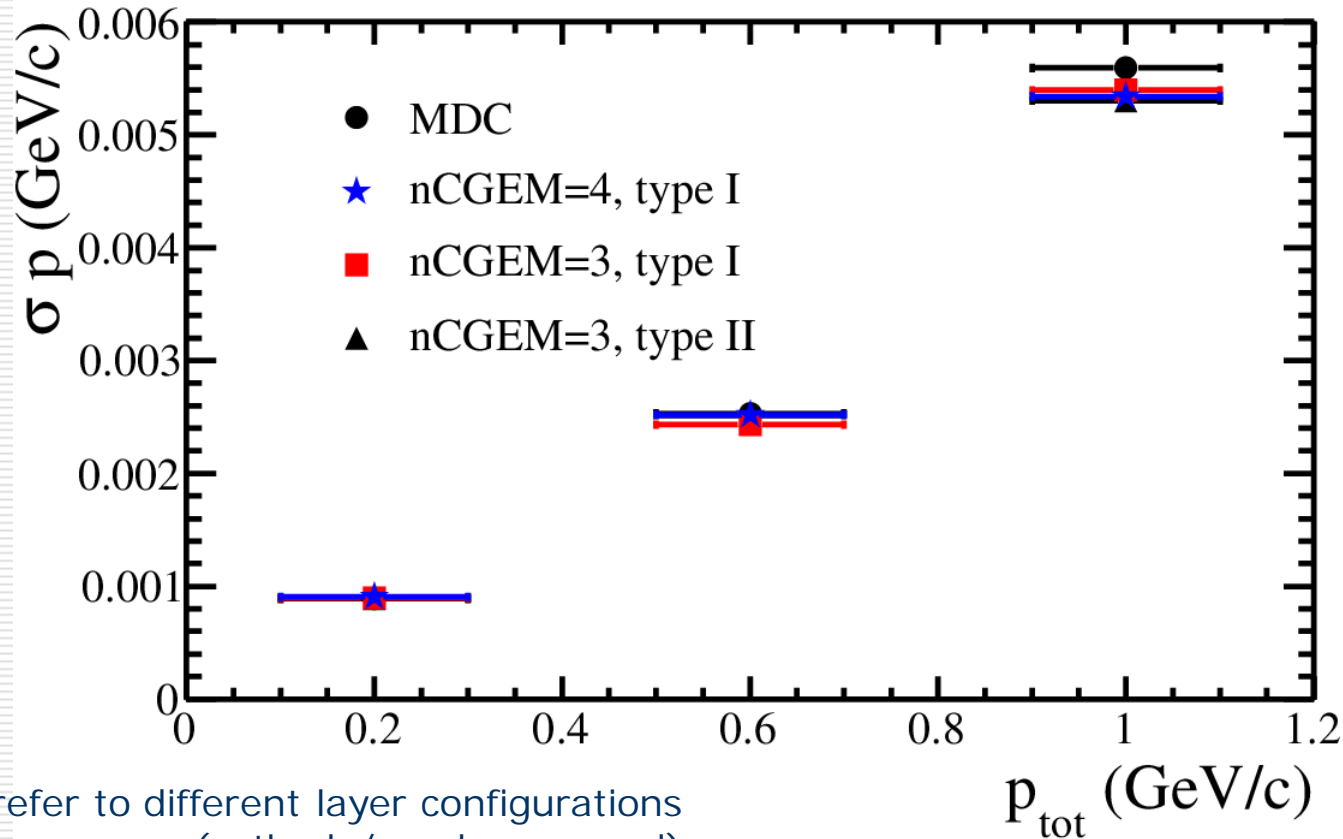


Readout	$\sigma_{r\phi}$ ( $\mu\text{m}$ )	$\sigma_z$ ( $\mu\text{m}$ )
Digital readout (Beam test @2009)	330	400
Analog readout (magnetic field effect avoided)*	80	150

\* Taken as expected spatial resolution



# Momentum resolution



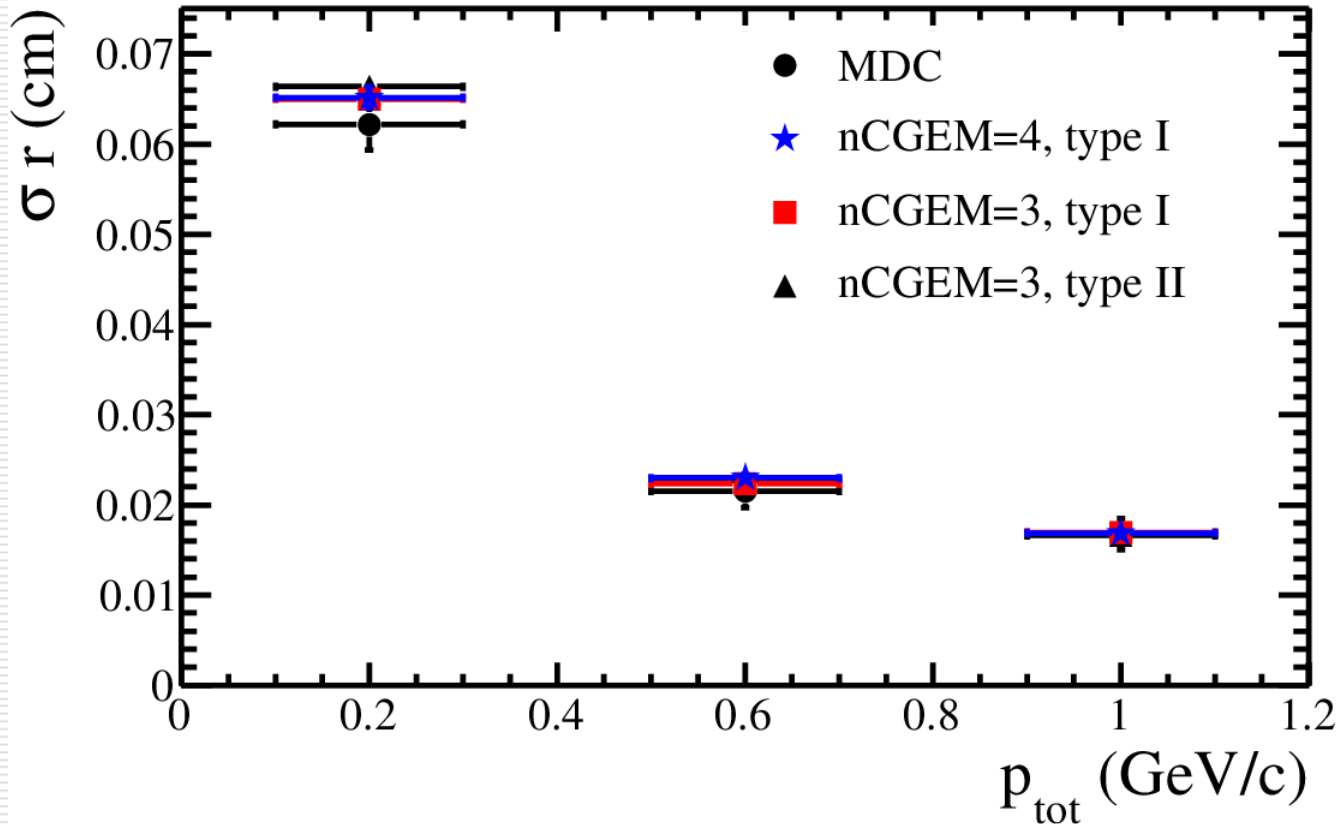
type I and II refer to different layer configurations  
(cathode/anode reversed)

$P_{tot}$ (GeV/c)	0.2	0.6	1.0
$\sigma_p$ (MDC) (MeV)	<b>0.89</b> (100%)	<b>2.53</b> (100%)	<b>5.59</b> (100%)
$\sigma_p$ (CGEM 4 layers, type I)	<b>0.91</b> (+2.2%)	<b>2.52</b> (-0.4%)	<b>5.34</b> (-4.5%)
$\sigma_p$ (CGEM 3 layers, type I)	<b>0.89</b> (+0%)	<b>2.43</b> (-3.9%)	<b>5.40</b> (-3.4%)
$\sigma_p$ (CGEM 3 layers, type II)	<b>0.89</b> (+0%)	<b>2.51</b> (-0.8%)	<b>5.30</b> (-5.2%)





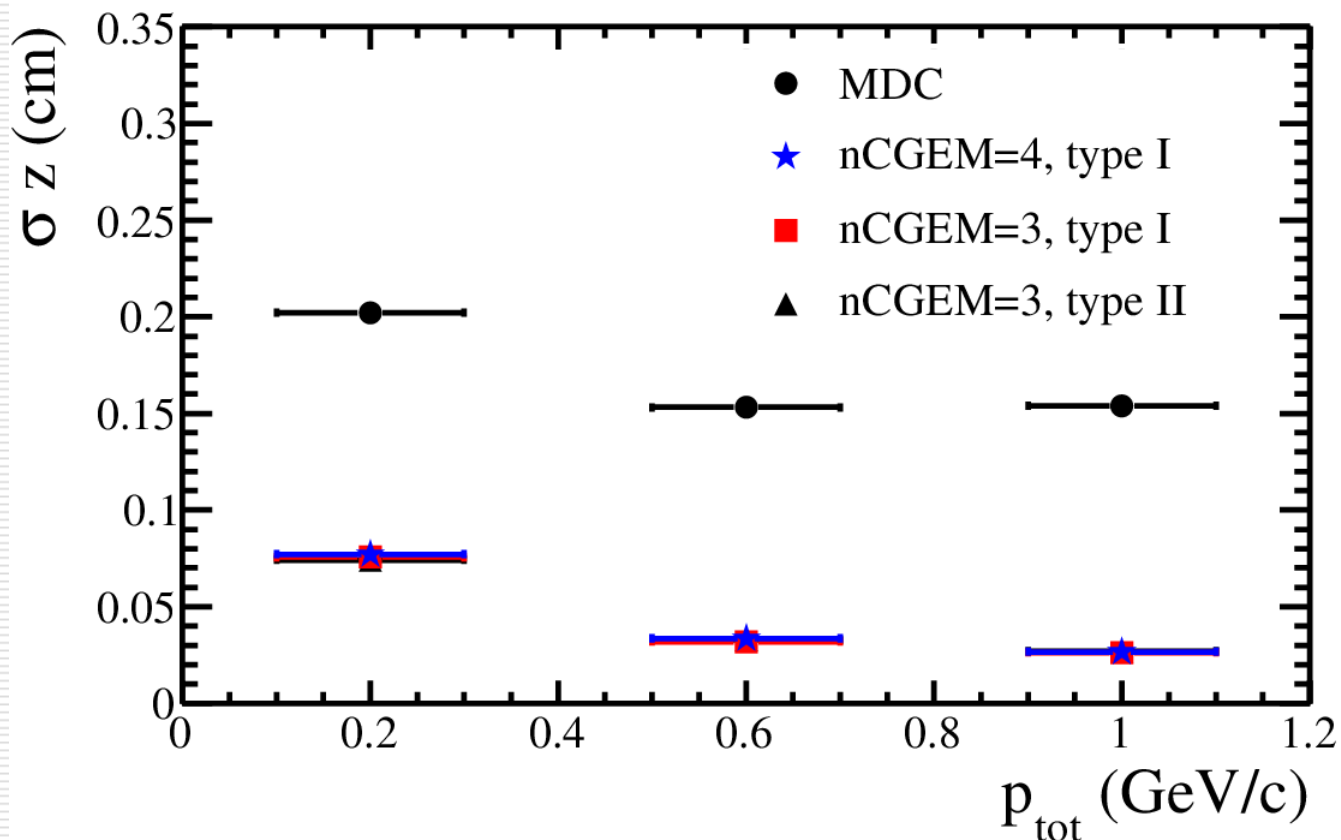
# Vertex resolution in r



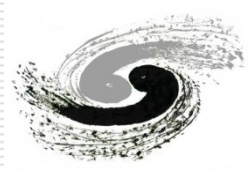
$P_{\text{tot}}$ (GeV/c)	0.2	0.6	1.0
$\sigma_r$ (MDC) ( $\mu\text{m}$ )	622 (100%)	215(100%)	168(100%)
$\sigma_r$ (CGEM 4 layers, type I)	652(+4.8%)	230(+7.0%)	169(+0.6%)
$\sigma_r$ (CGEM 3 layers, type I)	650(+4.5%)	224(+4.2%)	170(+1.2%)
$\sigma_r$ (CGEM 3 layers, type II)	664(+6.7%)	228(+6.0%)	166(-1.2%)



# Vertex resolution in z



$P_{\text{tot}}$ (GeV/c)	0.2	0.6	1.0
$\sigma_z$ (MDC) ( $\mu\text{m}$ )	2020 (100%)	1531 (100%)	1539 (100%)
$\sigma_z$ (CGEM 4 layers, type I)	772 (-61.8%)	336 (-78.1%)	266 (-82.7%)
$\sigma_z$ (CGEM 3 layers, type I)	757 (-62.5%)	319 (-79.2%)	263 (-82.9%)
$\sigma_z$ (CGEM 3 layers, type II)	742 (-63.3%)	327 (-78.6%)	270 (-82.5%)



## Conclusion

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CGEM inner detector (**VS** MDC inner detector)

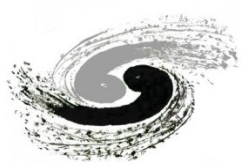
- **Improves dz resolution significantly**  
(by a factor of 2.6~6)
- **Comparable dr resolution**  
(~5% poorer for low momentum tracks)
- **Comparable momentum resolution**  
(~5% better for high momentum tracks)



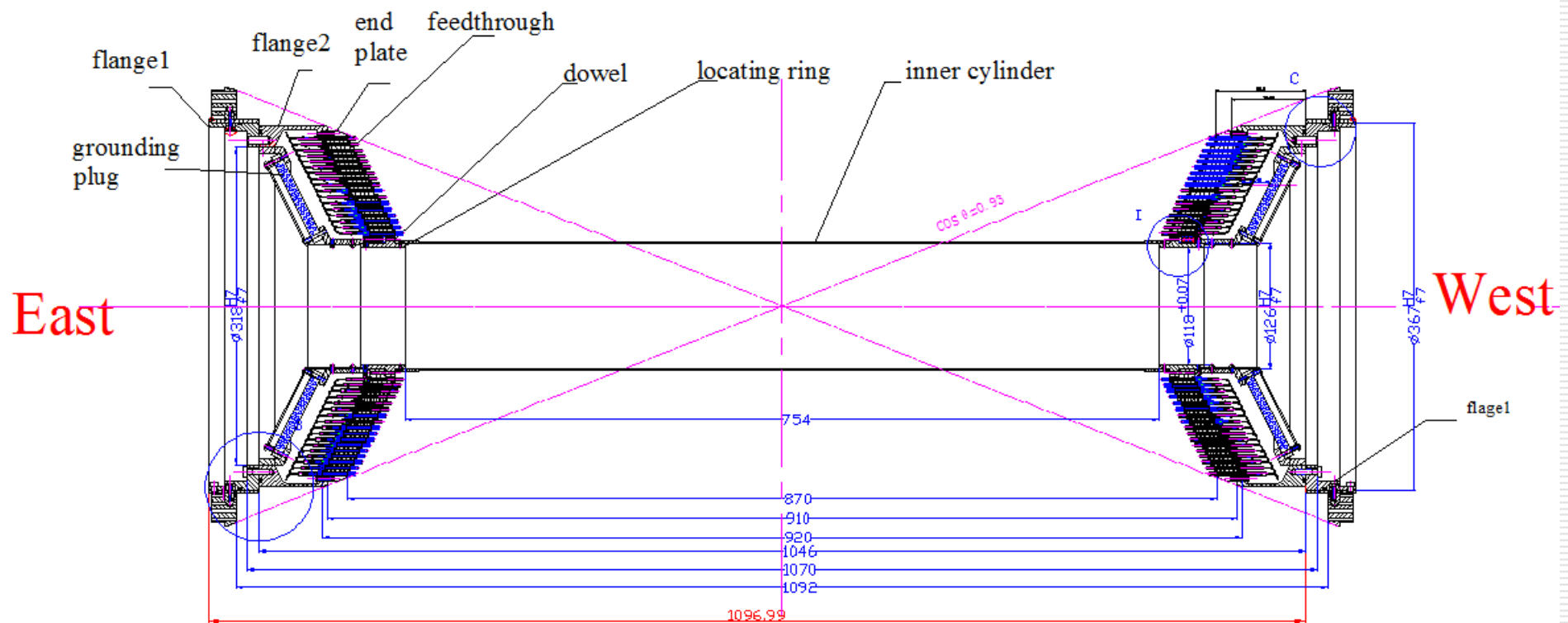
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# Space available for CGEM installation

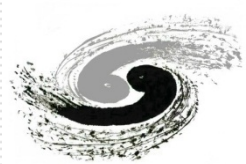
*Mingyi Dong*



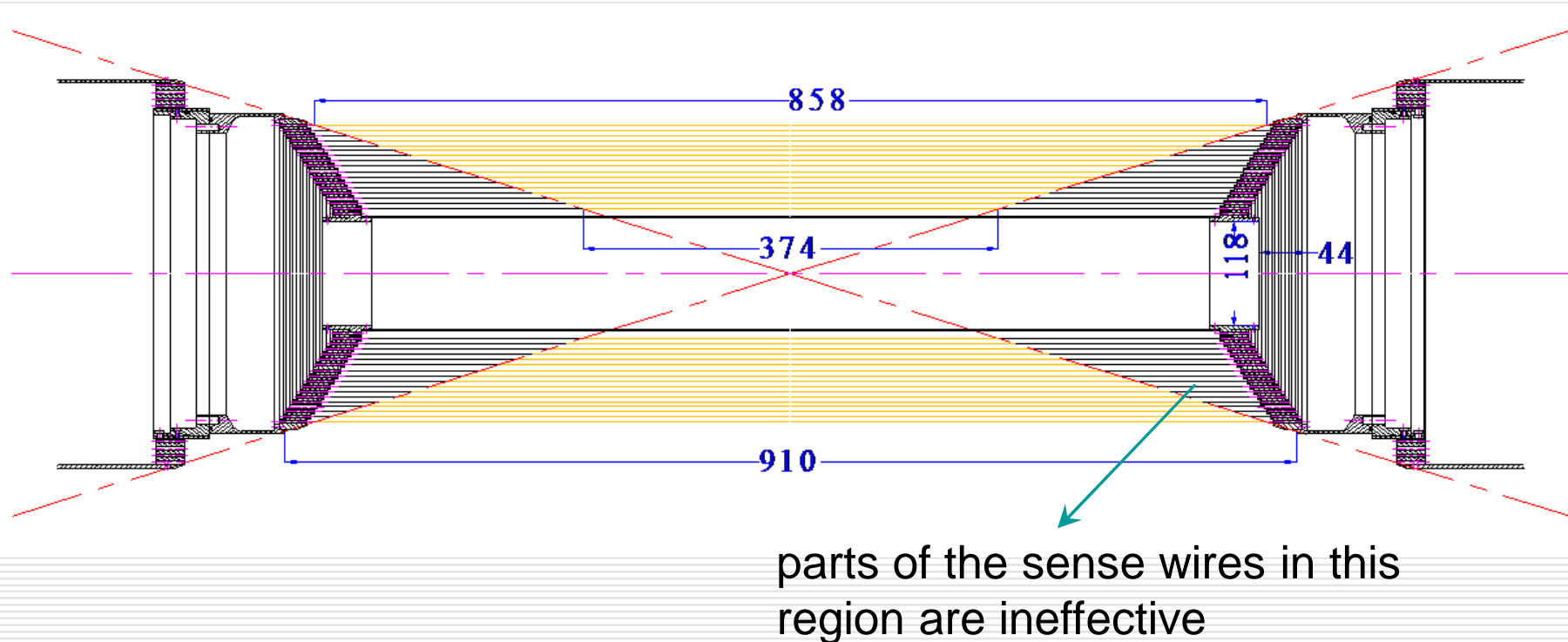
# Mechanical structure of inner MDC



- Inner drift chamber is fixed on the first step ring of MDC through two flanges



## Space available for CGEM



- ❑ Parts of inner sense wires in z direction are out of the effective solid angle ( $\cos\theta = 0.93$ )
- ❑ Also available space when we consider CGEM installation

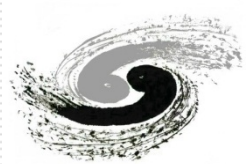


## Suppose 3 layers CGEM

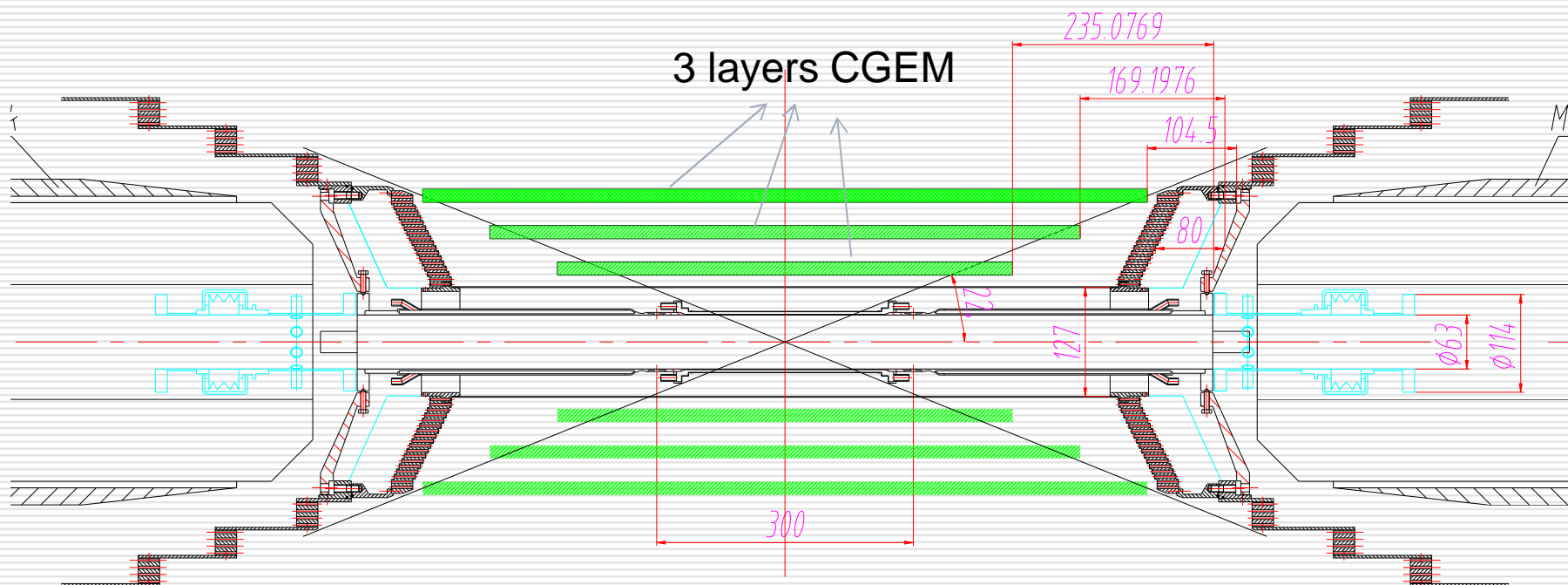
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Component	L1(Rin/Rout)	L2(Rin/Rout)	L3(Rin/Rout)	Material
Cathode	78.0/81.0	120.5/123.5	163.0/166.0	3 mm Honeycomb
Conversion + drift	81.0/84.0	123.5/126.5	166.0/169.0	3 mm gas
Transfer 1	84.0/86.0	126.5/128.5	169.0/171.0	2 mm gas
Transfer 2	86.0/88.0	128.5/130.5	171.0/173.0	2 mm gas
Induction	88.0/90.0	130.5/132.5	173.0/175.0	2 mm gas
Readout	90.0	132.5	175.0	
Outer shield	90.0/93.0	132.5/135.5	175.0/178.0	3 mm Honeycomb

- L1 length: 532mm , L2 length: 690mm
- L3 length: 847mm

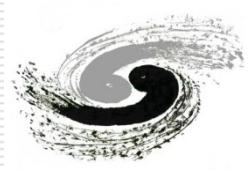


# Space available in z direction



- The gap is about 80mm~100mm between the end plate of MDC inner chamber and the mechanical support of beam pipe
- 105mm~235mm in z direction is available for the support structure, readout electronics, cables of CGEM on each side





## Summary

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- Space available for CGEM in r direction is about 63 (78?) mm ~183.5mm
- If the length of CGEM matches the MDC effective solid angle ( $\cos\theta = 0.93$ ), space available for CGEM in z direction is about 105mm~235mm on each side

# CGEM prototype project

# MAE Funding

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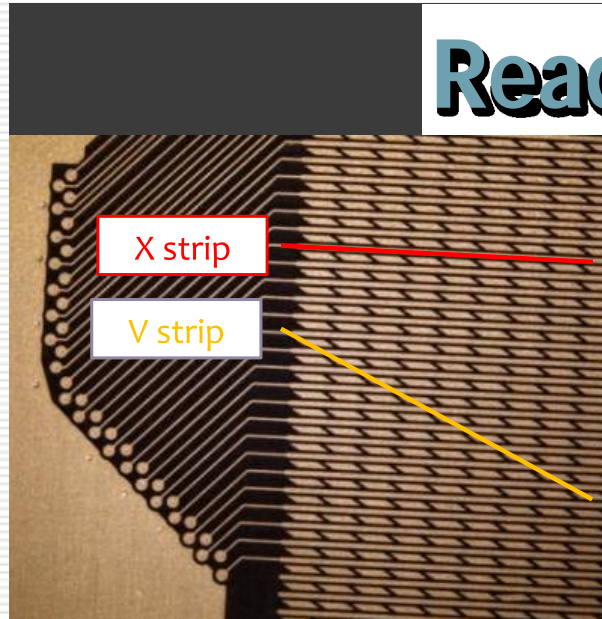
- June 2012: BESIII-Italy was invited by INFN management to apply for MAE(\*) money in a program for Chinese-Italian scientific cooperation
  - BESIII-Italy prepared 2 projects:
    - BESIII-To: “mobility of researchers”  
(physics analysis)
    - BESIII-LNF: “great relevance”  
(construction of one layer of cylindrical GEMs with analogic readout)
- Both projects have a BESIII-Chinese counterpart, submitted by BESIII-China to Chinese authorities
- (\*) Italian Ministry for Foreign Affairs  
April 2013: projects approved by MAE for funding

- The LNF project is for a total amount of 360 k€ divided over a period of 3 years.
- Every year MAE, INFN and IHEP contribute a budget of 40 k€ each
- MAE contribution for year “N” must be advanced by INFN in “N”, and refunded by MAE in “N+1”
- 2013: INFN will deliver in June the first 40 k€ of the 80 k€ expected (additional 40 k€ are expected later)
- These successes have motivated additional researchers from LNF and Torino and another INFN Institution, Ferrara, to apply for participation in BESIII. *Good news!* 😊

# New anode design

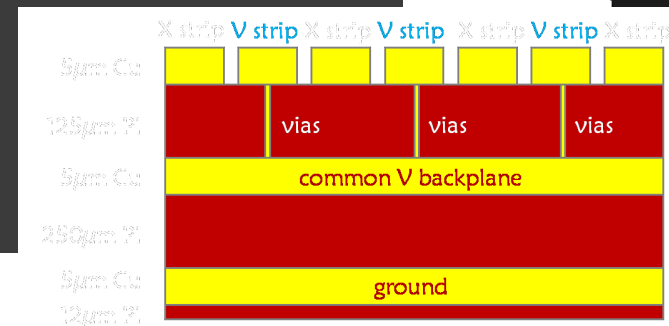
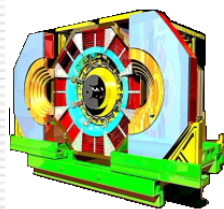
KLOE2 IT – Readout Plane (no more available)

## Readout Plane



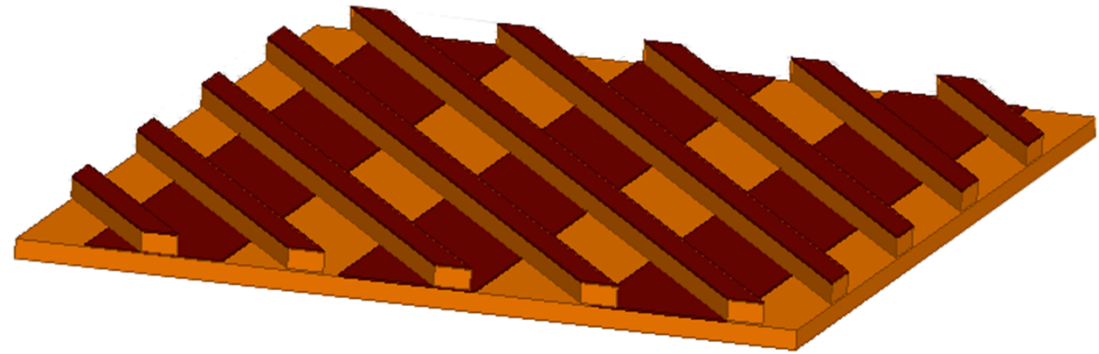
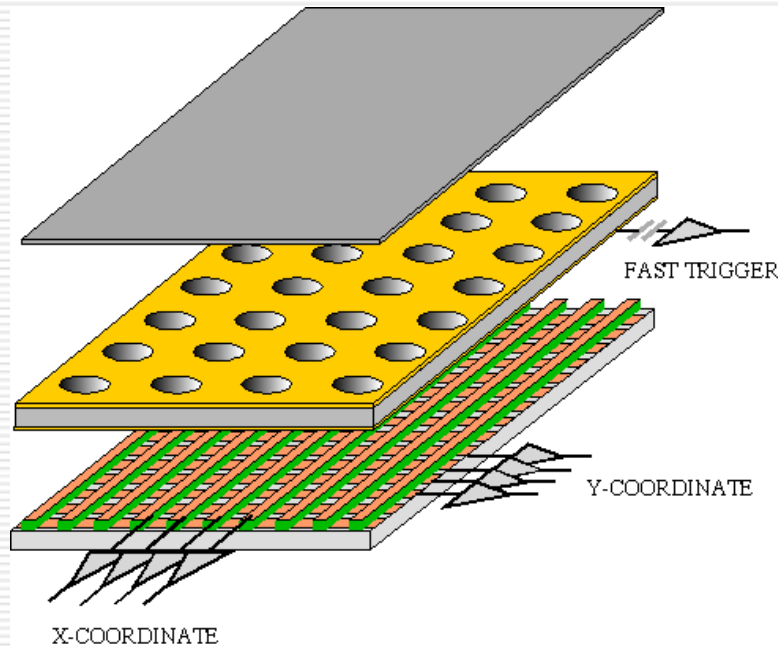
Readout plane is realized at CERN by PRE-EM  
It is a **kapton/copper multilayer flexible circuit**  
Provides 2-dimensional readout with strips on  
the same plane

- X are realized as longitudinal strips
- V are realized by connection of strips with  
conductive holes and a common backplane
- Pitch is 650  $\mu\text{m}$  for both



650  $\mu\text{m}$   $\rightarrow$  Y res 350  $\mu\text{m}$

## COMPASS-type solution: new readout electrodes



Pitch X strips: 650  $\mu\text{m}$   
 Width X strips: 350  $\mu\text{m}$   
 Width V strips: 80  $\mu\text{m}$   
 Kapton: 50  $\mu\text{m}$   
 Cu: 5  $\mu\text{m}$

- *Due to diffusion the charge cloud collected on the readout board is bigger than the strip width ( $\approx 3.5 \times \text{pitch}$ ) and a weighting method is used for calculate the exact track position in two dimensions*
- *Every single strip versus the other readout coordinate acts as a plate capacitor. With the permittivity  $\epsilon=3.9$  of Kapton and an area of  $2.27 \cdot 10^{-1} \text{cm}^2$ , this capacitance is 15.7 pF*

# Prototype roadmap to 2015

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- 2013
  - Cosmic ray setup: construction
  - Clean room preparation
  - Cylindrical prototype: design and tool construction
- 2014
  - Cosmic ray setup: run and analysis
  - Cylindrical prototype: construction and assembly
- 2015
  - Cylindrical prototype: test (cosmic rays and beam)

### A cosmic test setup (LNF-FE)

- The COMPASS-type readout layer is substantially different from the KLOE2 one
- It must be tested, before employ in a full-scale cylindrical prototype
- KLOE2 can give us a cosmic-ray telescope (3 complete and working planar GEM chambers of KLOE2 type)
- To these we will add a small (10x10 cm<sup>2</sup>) planar chamber with GEMs, and a COMPASS-type readout layer



- Buy planar GEM chamber kit from CERN
- Prepare support for readout layer
- Design and order COMPASS-type small readout layer
- Procure GASTONE, TOFPET analogic chips, etc.
- Buy boards for DAQ (ADCs or digitizers)
- Start taking cosmic data, beam if possible
- Small rate if few channels: will take time

## CGEM Prototype

- ❑ Start **simulations** with Garfield and Maxwell (LNF-FE)
- ❑ Test COMPASS-type readout plane with GASTONE analog chips (LNF-FE) and TOFPET using an amplifier
  - GASTONE analog chips can't be used in BESIII
- ❑ Start preliminary operations for construction of cylindrical prototype (LNF-FE)
- ❑ Develop DAQ for small test chamber (FE)
- ❑ Develop offline for small test chamber (LNF-FE)
- ❑ Test **rohacell-based technique** for cathode construction for the innermost layer and start a **mockup CGEM** (FE)

## Mechanics (FE-LNF-IHEP)

- ❑ Refurbish clean room, repair toolings etc
- ❑ Evaluate KLOE2 machine for inserting cylindrical layers (BESIII layers may be longer)
- ❑ Examine possibility to use KLOE2 molds for BESIII middle layer. Modify or rebuild if necessary!
- ❑ IHEP experienced manpower is strongly needed
- ❑ Start drawings for COMPASS-type readout layer

## Electronics, DAQ, Offline, HV, gas system

- Start ASIC development for BESIII (TO-LNF)
- Design off-detector electronics (LNF)
  
- Start studying impact on BESIII DAQ and L1 trigger (IHEP)
- Monte Carlo and Offline Reconstruction (IHEP, LNF, FE, TO)
- Plan additions to HV, Gas systems, Slow Controls (IHEP)

## IT FrontEnd electronics Plan (TO)

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- Readout ASIC for CGEM:
  - Modify/adapt the existing TOFPET ASIC  
(Rolo, Rivetti et al <http://iopscience.iop.org/1748-0221/8/02/C02050>)  
in IBM 130nm for PET applications
- Re-design a **new analogue FE** (suited for CGEM signals)
- Use of the **same BackEnd** of TOFPET
- Migration to a newer and cheaper technology:  
IBM 130nm → **UMC 110 nm**  
should be exportable in China (implemented in Italy)
- Integration and Development of such a Custom ASIC for CGEM

- Man Power is a critical Issue:
  - VLSI Lab at INFN-TO, led by A. Rivetti, has expertise on HEP applications
  - **Required** Man Power to work at VLSI Lab in Torino:
    - **3y\*1FTE** (PhD with electronics background) for testing/test bed
    - AND**
    - **2y\*1FTE** (experienced engineer in electronics) for chip development

□ ASIC Basic features:

- UMC 110 nm technology (limited power consumption, to be tested for radiation tolerance)
- Time and Charge measurements by independent TDCs
- TDC 50ps time binning
- Double threshold discrimination
- Time over Threshold (ToT) to measure the charge
- Power consumption ~ 7 mW p/channel

- **2013**
  - Planar GEM telescope instrumentation (signal & HV)
  - Readout electrodes simulation (parasitic minimization study)
  - TofPet asic (IT readout candidate) validation
- **2014**
  - IT prototype anode readout plane design (early 2014)
  - IT HV distribution and signal pick-up engineering (front-end integration)
  - Design of a new version of TofPet asic (different input stage to increase signal sensitivity and SNR) – 2 foundry submission foreseen
  - Off Detector Electronic design (end 2014)
    - ✦ Readout boards and Concentrators
    - ✦ HV distribution boards (minimize the number of HV primary channels)
  - IT prototype (sectors) instrumentation by means of existing asics developed for analog readout (APV25 ?)
- **2015**
  - New version of TofPet asic validation on CGEM IT prototype
  - Off Detector Electronics e HV design
  - IT front-end (TofPet) integration
- **2016**
  - On Detector (asic & boards) and Off Detector electronics production/test
- **2017**
  - IT instrumentation and validation



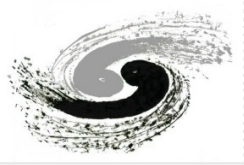
- 2013
    - start R&D program (cosmic setup, simulations)
    - write a Letter of Intent
  
  - 2014
    - build prototype with IT middle layer layout
  
  - 2015
    - Prototype test and validation
    - TDR
    - IT design and material procurement
  
  - 2016
    - start IT construction
  
  - 2017
    - complete IT, test and validation
-



## Cooperation activities from IHEP side (proposal)

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- verification of Manpower for:
  - building prototype
  - designing FE electronics
- implementing simulation and offline
- making DAQ and Slow Controls
- built up HV
- preparing Gas System
- installing CGEM



## Next step (proposal)

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- ❑ Preparation of a Letter of Intent for  
INFN-IHEP cooperation on CGEM
- ❑ Establishing a INFN-IHEP CGEM Steering Committee

# Letter of Intent (I)

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1. The present BESIII Drift Chamber
  - a. Description
  - b. Status of Inner Chamber
  - c. Present and future (high-Lum) problems for data taking
  
2. Estimation of backgrounds
  - a. Present backgrounds: measurements
  - b. Future backgrounds: simulations
  
3. The Cylindrical GEM technique: advantages and drawbacks
  - a. The KLOE2 Inner Tracker
  - b. Material budget
  - c. Analog vs. digital, expectations and measurements
  - d. Projects: geometry and layout
  
4. Simulation of Cylindrical GEM Tracker
  - a. Parametric simulations
  - b. Single particle simulations
  - c. Problematic channels simulations

## Letter of Intent (II)

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5. Frontend electronics
  - a. The KLOE2 electronics
  - b. Requirements for BESIII
  
6. Impact on DAQ
  - a. Dead time
  - b. Disk space
  
7. Requirements on trigger
  - a. Intermediate level trigger
  
8. Offline reconstruction
  
9. Money, manpower, times, tasks subdivision.....

**Thanks for**

**谢谢**

**your attention**